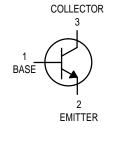
VHF/UHF Transistor

NPN Silicon





MMBTH10LT1

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	25	Vdc
Collector-Base Voltage	VCBO	30	Vdc
Emitter-Base Voltage	V _{EBO}	3.0	Vdc

DEVICE MARKING

MMBTH10LT1 = 3EM

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Total Device Dissipation FR-5 Board ⁽¹⁾ T _A = 25°C	PD	225	mW	
Derate above 25°C		1.8	mW/°C	
Thermal Resistance Junction to Ambient	R _{0JA}	556	°C/W	
Total Device Dissipation Alumina Substrate, ⁽²⁾ $T_A = 25^{\circ}C$	PD	300	mW	
Derate above 25°C		2.4	mW/°C	
Thermal Resistance Junction to Ambient	R _{θJA}	417	°C/W	
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C	

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS					

Collector–Emitter Breakdown Voltage ($I_C = 1.0 \text{ mAdc}, I_B = 0$)	V(BR)CEO	25	_	—	Vdc
Collector–Base Breakdown Voltage ($I_C = 100 \ \mu Adc, I_E = 0$)	V(BR)CBO	30	_	—	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \ \mu Adc$, $I_C = 0$)	V(BR)EBO	3.0	_	—	Vdc
Collector Cutoff Current ($V_{CB} = 25 \text{ Vdc}, I_E = 0$)	ICBO	—	_	100	nAdc
Emitter Cutoff Current (V_{EB} = 2.0 Vdc, I_C = 0)	IEBO	—	-	100	nAdc

1. FR–5 = 1.0 x 0.75 x 0.062 in.

2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina

Thermal Clad is a registered trademark of the Berquist Company.

Preferred devices are Motorola recommended choices for future use and best overall value.



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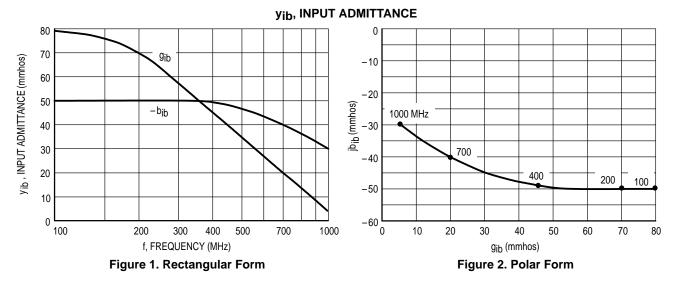
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

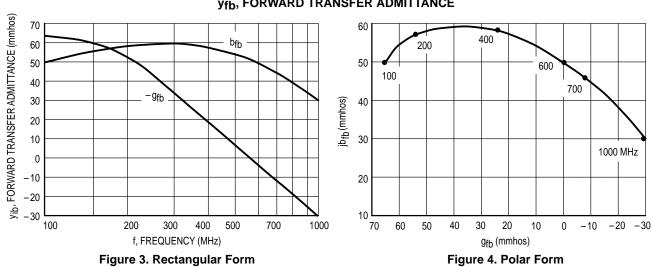
Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS				•	•
DC Current Gain (I_C = 4.0 mAdc, V_{CE} = 10 Vdc)	hFE	60	—	-	—
Collector–Emitter Saturation Voltage ($I_C = 4.0 \text{ mAdc}$, $I_B = 0.4 \text{ mAdc}$)	VCE(sat)	—	—	0.5	Vdc
Base–Emitter On Voltage (I_C = 4.0 mAdc, V_{CE} = 10 Vdc)	VBE	—	—	0.95	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current–Gain – Bandwidth Product (I _C = 4.0 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	fT	650	—	-	MHz
Collector–Base Capacitance (V_{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{cb}	—	—	0.7	pF
Common–Base Feedback Capacitance $(V_{CB}= 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{rb}	—	—	0.65	pF
Collector Base Time Constant (I_C = 4.0 mAdc, V_{CB} = 10 Vdc, f = 31.8 MHz)	rb′C _C	—	—	9.0	ps

TYPICAL CHARACTERISTICS

COMMON-BASE y PARAMETERS versus FREQUENCY

(V_{CB} = 10 Vdc, I_C = 4.0 mAdc, T_A = 25°C)





yfb, FORWARD TRANSFER ADMITTANCE

TYPICAL CHARACTERISTICS



(V_{CB} = 10 Vdc, I_C = 4.0 mAdc, T_A = 25°C)



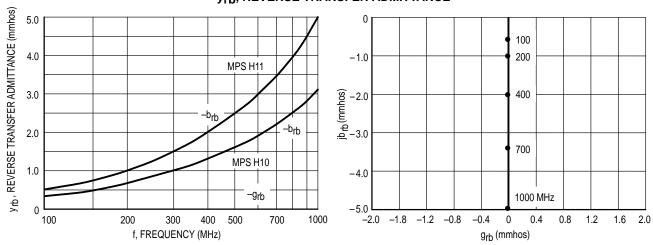


Figure 5. Rectangular Form



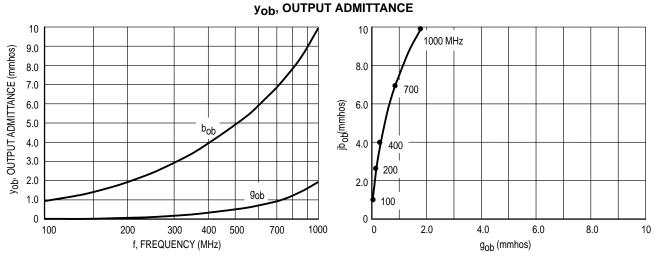


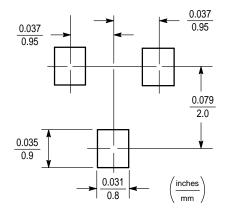
Figure 7. Rectangular Form

Figure 8. Polar Form

INFORMATION FOR USING THE SOT-23 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.





SOT-23 POWER DISSIPATION

The power dissipation of the SOT–23 is a function of the drain pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_J(max)$, the maximum rated junction temperature of the die, $R_{\theta}JA$, the thermal resistance from the device junction to ambient, and the operating temperature, T_A . Using the values provided on the data sheet for the SOT–23 package, PD can be calculated as follows:

$$P_{D} = \frac{T_{J(max)} - T_{A}}{R_{\theta}JA}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature T_A of 25°C, one can calculate the power dissipation of the device which in this case is 225 milliwatts.

$$P_{D} = \frac{150^{\circ}C - 25^{\circ}C}{556^{\circ}C/W} = 225 \text{ milliwatts}$$

The 556°C/W for the SOT–23 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 225 milliwatts. There are other alternatives to achieving higher power dissipation from the SOT–23 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad[™]. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

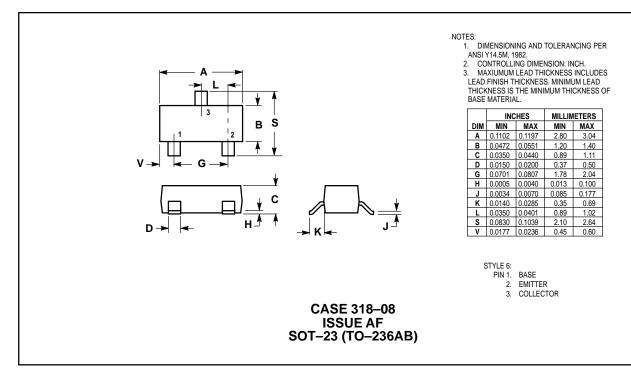
SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.

* Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

PACKAGE DIMENSIONS



MMBTH10LT1

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